TOSHIBA PHOTOCOUPLER GaAlAs LED & PHOTO-IC

TLP708,TLP708F

Factory Automation (FA)
Home Electrical Appliances
Operates at high ambient temperatures up to 125°C

The Toshiba TLP708 consists of a GaA ℓ As light emitting diode and an integrated high-gain, high-speed photodetector. The TLP708 is housed in the SDIP6 package.

Compared to the standard DIP8 package, TLP708 is smaller in size, yet comes with international safety standards under a reinforced isolation category. As such, it is possible to reduce the mounting footprint for applications that require certifications for safety standards.

The photodetector has an open-collector output stage, and an internal Faraday shield that provides a guaranteed common-mode transient immunity of ± 15 kV/µs. As TLP708 is also able to operate up to $125^{\circ}\mathrm{C}$, it is suitable for use in applications like industrial equipments where it is necessary to operate under high ambient temperatures.

TLP708F is of a long creepage distance and clearance distance type.

• Input threshold current: IFHL = 5mA (Max)

• Switching time (t_{pHL}/t_{pLH}): 75ns (Max)

• Data transfer rate: 15 MBd (Typ.)

Guaranteed Performance over temperature: -40 to 125° C

Power supply voltage: 4.5 to 5.5V

 \bullet Common mode transient immunity: $$\pm15~\mathrm{kV}$ /µs (Min)

Isolation voltage: $5000 V_{rms}$ (Min)

• Construction mechanical rating

	7.62 mm Pitch	10.16 mm Pitch
	TLP708 Type	TLP708F Type
Creepage distance Clearance distance Insulation thickness	7.0 mm (min) 7.0 mm (min) 0.4 mm (min)	8.0 mm (min) 8.0 mm (min) 0.4 mm (min)

• UL recognized: UL1577, File No. E67349

• cUL recognized: CSA Component Acceptance Service No.5A,

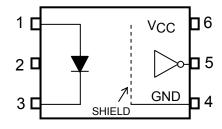
File No. E67349

• Option (D4) Type

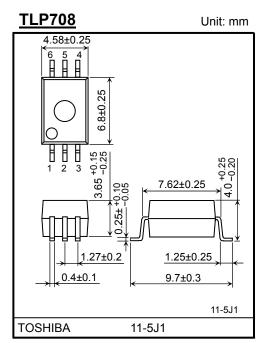
VDE EN60747-5-2: under application

(Note) When EN60747-5-2 approval type is needed, please designate "Option (D4)".

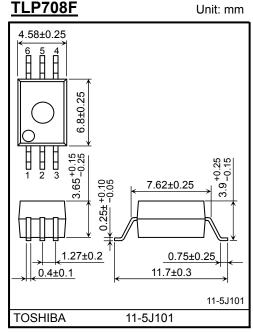
Pin Configuration (Top View)



1:ANODE 2:N.C. 3:CATHODE 4:GND 5:V_O(Output) 6:V_{CC}

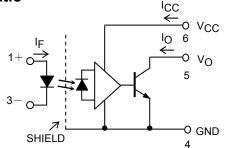


Weight: 0.26 g (typ.)



Weight: 0.26 g (typ.)

Schematic



Truth Table

Input	Output
Н	L
L	Н

Note: A $0.1\mu F$ bypass capacitor must be connected between pins 6 and 4.

Absolute Maximum Ratings (Ta = 25℃)

	Characteristics	Symbol	Rating	Unit	
	Forward Current	(Ta < 110℃)	Ιϝ	25	mA
	Forward Current Derating	(Ta ≥ 110°C)	ΔIF/°C	-0.67	mA/℃
	Pulse Forward Current (Note 1)	(Ta < 110℃)	IFP	50	mA
LED	Pulse Forward Current Derating	(Ta ≥ 110°C)	∆l _{FP} /°C	-1.34	mA/°C
	Reverse Voltage		VR	5	V
	Input Power Dissipation	(Ta < 110℃)	PD	40	mW
	Input Power Dissipation Derating	(Ta ≥ 110°C)	∆PD/°C	-1.0	mW/℃
	Output Current	(Ta ≤ 125°C)	IO	25	mA
Output Voltage		VO	6	V	
ECT(Output Voltage Supply Voltage Output Power Dissipation (Ta < 110°C)		VCC	6	V
ETE	Output Power Dissipation	(Ta < 110℃)	PO	80	mW
	Output Power Dissipation Derating	(Ta ≥ 110°C)	ΔP _O /°C	-2.0	mW/℃
Ope	erating Temperature Range	T _{opr}	-40 to 125	$_{\mathbb{C}}$	
Stor	age Temperature Range	T _{stg}	-55 to 150	$^{\circ}$ C	
Lea	d solder Temperature (10s)	T _{sol}	260	$_{\mathbb{C}}$	
Isola	ation voltage (Note 2)		BVS	5000	V _{rms}

Note: Using continuously under heavy loads (e.g. an application of high temperature/current/voltage and a significant change in temperature, etc.) may cause this product to decrease in reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report

Note 1: Pulse width ≤ 1ms, duty=50%

Note 2: R.H. ≤ 60%, Ta = 25°C, AC 1 min

and estimated failure rate, etc)

This device is regarded as a two-terminal device: pins 1, 2 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.



Recommended Operating Condition

Characteristics	Symbol	Min.	Тур.	Max.	Unit
'L' level input voltage	V_{FL}	0	_	0.8	V
'H' level input current	l _{FH}	7.5	_	15	mA
Supply voltage*	VCC	4.5	_	5.5	V
Operating temperature range	T _{opr}	-40	_	125	$^{\circ}$

^{*} This item denotes operating ranges, not meaning of recommended operating conditions.

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. In addition, each item is an independent guideline. In developing designs using this product, please confirm the specified characteristics shown in this document.

Electrical Characteristics

(Unless otherwise specified, Ta = -40 to 125° C, VCC = 4.5 to 5.5V)

(3111303 3111311113 3p3311134, 14 43 12 3, 16 4, 16 6 3131)							
Characteristic	Symbol	Test Circuit	Test Conditions	Min.	Тур.*	Max.	Unit
Input forward current	V _F	_	I _F = 10 mA, Ta = 25℃	1.40	1.57	1.80	V
Temperature coefficient of forward voltage	ΔV _F /ΔTa	_	I _F = 10 mA	_	-1.8	_	mV/°C
Input reverse current	IR	_	V _R = 5 V, Ta = 25℃	_	_	10	μA
Input capacitance	CT	_	$V_F = 0 V$, $f = 1 MHz$, $Ta = 25^{\circ}C$	_	60	_	pF
			V _F = 0.8 V, V _O = 5.5 V		_	250	
"H" level output current	ЮН	1	$V_F = 0.8 \text{ V}, V_O = 5.5 \text{ V}$ Ta = 25°C	_	0.5	10	μΑ
"L" level output voltage	V _{OL}	2	I _F = 10 mA I _{OL} = 13 mA (sink)	_	0.3	0.6	V
Input threshold current	l _{FHL}	_	I _{OL} = 13 mA (sink) V _O < 0.6 V	_	1.5	5.0	mA
"H" level supply current	ICCH	3	I _F = 0 mA	_	1.5	5.0	mA
"L" level supply current	ICCL	4	I _F = 10 mA	_	1.4	5.0	mA

^{*}All typical values are at Ta=25°C, V_{CC}=5V unless otherwise specified.

Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Capacitance input to output	CS	$V_S = 0V$, $f = 1MHz$ (Note 2)	_	0.8	_	pF
Isolation resistance	RS	R.H. ≤ 60%,V _S = 500V (Note 2)	1×10 ¹²	10 ¹⁴	_	Ω
		AC,1 minute	5000	_	_	V _{rms}
Isolation voltage BV	BVS	AC,1 second,in oil	_	10000	_	V.,
		DC,1 minute,in oil	_	10000	_	V _{dc}

3

Switching Characteristics

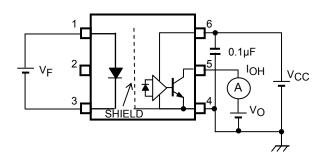
(Unless otherwise specified, Ta = -40 to $125^{\circ}C$, VCC = 4.5 to 5.5 V)

Characteristic	Symbol	Test Circuit	Test Cor	nditions	Min.	Typ.*	Max.	Unit
Propagation delay time to logic low output	^t pHL		I _F =0→7.5mA	R _L =350Ω	_	35	75	ns
Propagation delay time to logic high output	^t pLH		I _F =7.5→0mA	C _L =15pF (Note 4)	_	35	75	ns
Switching time dispersion between ON and OFF	t _{pHL} - t _{pLH}	5	I _F =0↔7.5mA	R _L =350Ω C _L =15pF	_	12	35	ns
Propagation delay skew (Note 5)	t _{psk}			(Note 4)	-50	_	50	ns
Output fall time (90-10%)	t _f		I _F =0→7.5mA	R _L =350Ω	_	6	_	ns
Output rise time (10-90%)	t _r		I _F =7.5→0mA	5→0mA C _L =15pF (Note 4)	_	18		ns
Common mode transient immunity at high level output	СМ _Н	V_{CM} =1000 V_{P-P} , I_{F} =0 mA , V_{CC} =5 V , T_{a} =25 $^{\circ}$ C		•	+15	_	-	kV/μs
Common mode transient immunity at low level output	CML	О	6 VCM=1000Vp-p , IF=10mA, V _{CC} =5V , Ta=25℃		-15	_	_	kV/µs

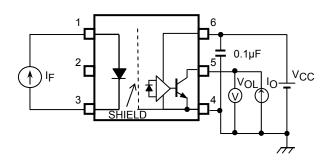
^{*}All typical values are at Ta=25°C

- Note 3 : A ceramic capacitor $(0.1\mu\text{F})$ should be connected from pin 6 (V_{CC}) to pin 4 (GND) to stabilize the operation of the high gain linear amplifier. Failure to provide the bypass may impair the switching property. The total lead length between the capacitor and coupler should not exceed 1 cm.
- Note 4 : f=5MHz, duty=50%, input current t_Γ = t_f =4.5ns,
 - C_L is approximately 15pF which includes probe and jig/stray wiring capacitance.
- Note 5: Propagation delay skew is defined as the difference between the largest and smallest propagation delay times (i.e. t_{pHL} or t_{pLH}) of multiple samples. Evaluations of these samples are conducted under identical test conditions (supply voltage, input current, temperature, etc).

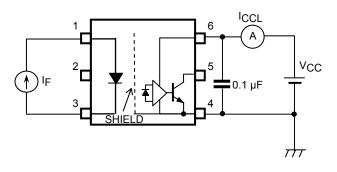
TEST CIRCUIT 1: IOH Test Circuit



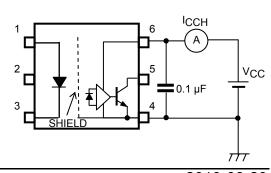
TEST CIRCUIT 2: Vol Test Circuit



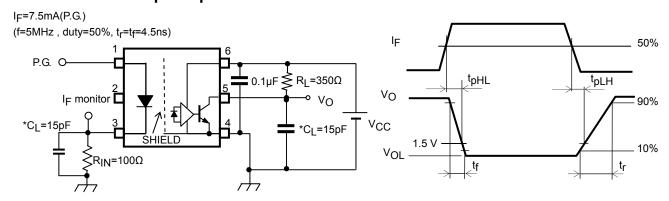
TEST CIRCUIT 3: ICCL Test Circuit



TEST CIRCUIT 4: ICCH Test Circuit

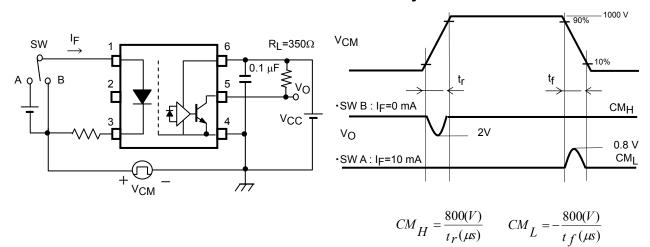


TEST CIRCUIT 5: tpHL, tpLH Test Circuit



 \mathbf{C}_{L} includes probe and stray capacitance. P.G.: Pulse generator

TEST CIRCUIT 6: Common-Mode Transient Immunity Test Circuit

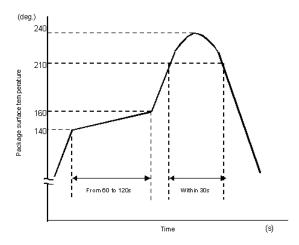


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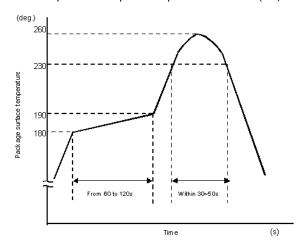
PRECAUTIONS OF SURFACE MOUNTING TYPE PHOTOCOUPLER SOLDERING & GENERAL STORAGE

(1) Precautions for Soldering

- 1) When Using Soldering Reflow
 - An example of a temperature profile when Sn-Pb eutectic solder is used:



• An example of a temperature profile when lead(Pb)-free solder is used:



- Reflow soldering should be performed no more than twice.
- The mounting should be completed with the interval from the first to the last mountings being 2 weeks.
- 2) When using soldering flow (Applicable to both eutectic solder and lead (Pb)-free solder)
 - Apply preheating of 150°C for 60 to 120 seconds.
 - Mounting condition of 260°C or less within 10 seconds is recommended.
 - Flow soldering should be performed no more than once.
- 3) When using soldering iron (Applicable to both eutectic solder and Lead(Pb)-Free solder)
 - Complete soldering within 10 seconds for lead temperature not exceeding 260°C or within 3 seconds not exceeding 350°C.
 - Heating by soldering iron should be performed no more than once per lead.

(2) Precautions for General Storage

- 1) Do not store devices in places where they will be exposed to moisture or direct sunlight.
- 2) During transportation or storage of devices, follow the cautions indicated on the carton box.
- 3) The storage area temperature should be kept within a temperature range of 5°C to 35°C, and the relative humidity should be maintained between 45% and 75%.
- 4) Do not store devices in the presence of harmful (especially corrosive) gases, or under dusty conditions.
- 5) Use storage areas where there is minimal temperature fluctuation. The solderability of the leads will be degraded as rapid temperature changes can cause condensation to form on the stored devices, resulting in lead oxidation or corrosion.
- 6) When repacking devices, use anti-static containers.
- 7) Do not apply any external force or load directly to devices when they are in storage.
- 8) If devices have been stored for more than two years, it is recommended that their solderability be tested before they are used even if the above precautions have been followed.

Specifications for Embossed-Tape Packing (TP) for SDIP6 Type Photocoupler

1. Applicable Package

Package Name	Product Type
SDIP6	Photocouplers

2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.



3. Tape Dimensions

3.1 Orientation of Devices in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.

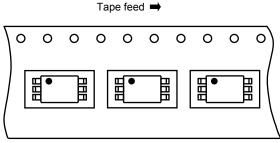


Figure 1 Device Orientation

3.2 Tape Packing Quantity: 1500 devices per reel

3.3 Empty Device Recesses Are as Shown in Table 1.

Table 1 Empty Device Recesses

Item	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0	Within any given 40-mm section of tape, not including leader and trailer
Single empty device recesses	6 devices (max) per reel	Not including leader and trailer

3.4 Start and End of Tape:

The start of the tape has 30 or more empty holes. The end of the tape has 30 or more empty holes and two empty turns as a cover tape.

3.5 Tape Specification

- (1) Tape material: Plastic (protection against electrostatics)
- (2) Dimensions: The tape dimensions are as shown in Figure 2 and Table 2.

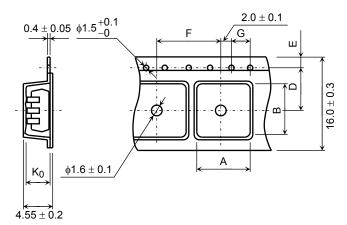


Figure 2 Tape Forms

Table 2 Tape Dimension

 $\begin{array}{c} \text{Unit: mm} \\ \text{Unless otherwise specified: } \pm 0.1 \end{array}$

Symbol Dimension Remark 10.4 Α В 5.1 7.5 Center line of indented square hole and sprocket hole D Е 1.75 Distance between tape edge and hole center Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes F 12.0 +0.1 -0.3 (max) per 10 feed holes G 4.0 Cumulative error

Internal space

9

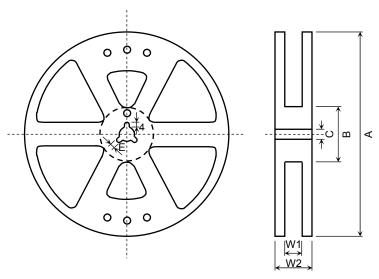
4.1

 K_0



3.6 Reel

- (1) Material: Plastic
- (2) Dimensions: The reel dimensions are as shown in Figure 3 and Table 3.



Unit: mm Symbol Dimension $\phi 380 \pm 2$ Α В $\phi 80 \pm 1$ С $\phi 13 \pm 0.5$ $2.0\pm0.5\,$ U 4.0 ± 0.5 W1 17.5 ± 0.5 W2 $21.5\pm1.0\,$

Table 3 Reel Dimension

Figure 3 Reel Forms

4. Packing

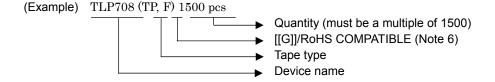
Either one reel or five reels of photocouplers are packed in a shipping carton.

5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Method

When placing an order, please specify the product number, the CTR rank, the tape type and the quantity as shown in the following example.



Note 6 :Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

10

RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

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