#### High static dV/dt > 1.5 kV/µs Input sensitivity I<sub>FT</sub> = 5 mA and 10 mA 4 MT2 On-state RMS current I<sub>T(RMS)</sub> = 70 mA

Optocoupler, Phototriac Output, Non-Zero Crossing, 1.5 kV/µs dV/dt, 600 V

- 600 V peak off-state blocking voltage
- Isolation test voltage 3750 V<sub>BMS</sub>
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- · Consumer appliances
- Triac drives

**FEATURES** 

- · Solid-state relays
- Industrial controls
- Office equipment
- Lighting controls

#### AGENCY APPROVALS

- UL1577, file no. E52744, double protection (pending)
- cUL-file no. E52744, equivalent to CSA bulletin 5A (pending)
- VDE 0884-5, DIN EN 60747-5-5
- CQC (pending)

ORDERING INFORMATION											
	V	0	Μ	3	0	5	X	Т	SOP-#		
				PART N	UMBER				7.21 mm ►		
AGENCY CERTIFIED/PACKAGE			KAGE	TRIGGER CURRENT IFT							
UL, cl	UL, CQC				5 mA	1			10 mA		
SOP-4				VOM3053T				VOM3052T			
VDE, UL, cUL, CQC				5 mA				10 mA			
SOP-4				VOM3053-X001T				VOM3052-X001T			

#### Notes

i179066

VOM3052.

DESCRIPTION

- For additional information on the available options refer to option information.
- The product is available only on tape and reel.

3 MT1



COMPLIANT GREEN (5-2008)

Vishay Semiconductors



Αl 1

C 2

The VOM3052 and VOM3053 phototriac consist a GaAs

IRLED optically coupled to a photosensitive non-zero

crossing TRIAC packaged in a SOP-4 package. It has

a LED trigger current of 5 mA for VOM3053 and 10 mA for

The VOM3052 and VOM3053 phototriac isolate low-voltage logic from 120 V<sub>AC</sub>, 240 V<sub>AC</sub>, and 380 V<sub>AC</sub> lines to control

resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.



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## **Vishay Semiconductors**

ABSOLUTE MAXIMUM RAT	<b>INGS</b> (T <sub>amb</sub> = 25 °C, unle	ss otherwise s	specified)		
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT					
Reverse voltage			V <sub>R</sub>	6	V
Forward current			lF	60	mA
Peak surge current	100 µs, 200 pps		I <sub>FSM</sub>	0.5	А
Power dissipation			P <sub>diss</sub>	100	mW
OUTPUT					
Peak off-state voltage			V <sub>DRM</sub>	600	V
RMS on-state current			I <sub>T(RMS)</sub>	70	mA
Peak non-repetitive surge current	PW = 100 ms, 120 pps		I <sub>TSM</sub>	1	А
Power dissipation			P <sub>diss</sub>	200	mW
COUPLER					
Isolation test voltage	t = 1 min		V <sub>ISO</sub>	3750	V <sub>RMS</sub>
Power dissipation			P <sub>tot</sub>	300	mW
Storage temperature range			T <sub>stg</sub>	- 55 to + 150	°C
Ambient temperature range			T <sub>amb</sub>	- 40 to + 100	°C
Soldering temperature <sup>(1)</sup>			T <sub>sld</sub>	260	°C

#### Notes

Stresses in excess of the Absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to Absolute
Maximum Ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to "Assembly Instructions" for surface mounted devices (<u>www.vishay.com/doc?80054</u>).

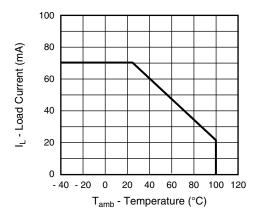


Fig. 1 - Recommended Operating Condition



# Vishay Semiconductors

ELECTRICAL CHARACTER	RISTICS (T <sub>amb</sub> = 25 °C, ur	nless otherwis	se specifie	d)			
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I <sub>F</sub> = 10 mA		V <sub>F</sub>		1.2	1.5	V
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>			10	μA
Input capacitance	$V_F = 0 V$ , f = 1 MHz		CI		25		pF
OUTPUT							
Off-state current	$V_D = V_{DRM}$		I <sub>DRM</sub>			100	nA
On-state voltage	l <sub>T</sub> = 100 mA		V <sub>TM</sub>			3	V
Critical rate of rise off-state voltage	$V_D = 0.67 V_{DRM}, T_J = 25 \ ^{\circ}C$		dV/dt <sub>cr</sub>	1500			V/µs
Critical rate of rise of voltage at current commutation			dV/dt <sub>crq</sub>		0.13		V/µs
COUPLER							
LED trigger current,		VOM3053	I <sub>FT</sub>			5	mA
current required to latch output	$V_D = 3 V$	VOM3052	I <sub>FT</sub>			10	mA
Capacitance (input - output)	f = 1 MHz, V <sub>IO</sub> = 0 V		C <sub>IO</sub>		0.8		pF
Peak off-state voltage	I <sub>C</sub> = 100 μA		V <sub>DRM</sub>	600			V
Holding current			I <sub>hold</sub>		0.3		mA

Note

• Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SAFETY AND INSULATION RATINGS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)			40/100/21		
Pollution degree (DIN VDE 0109)			2		
Comparative tracking index	CTI	175		399	
Peak transient overvoltage	V <sub>IOTM</sub>			6000	V <sub>peak</sub>
Peak insulation voltage	V <sub>IORM</sub>			707	V <sub>peak</sub>
Isolation resistance at $T_{amb}$ = 100 °C, $V_{DC}$ = 500 V	R <sub>IO</sub>	10 <sup>11</sup>			Ω
Isolation resistance at $T_{amb}$ = 25 °C, $V_{DC}$ = 500 V	R <sub>IO</sub>	10 <sup>12</sup>			Ω
Safety rating - power	P <sub>SO</sub>			400	mW
Safety rating - input current	I <sub>SI</sub>			150	mA
Safety rating - temperature	T <sub>SI</sub>			165	°C
Creepage distance		5			mm
Clearance distance		5			mm
Insulation thickness		0.4			mm



**Vishay Semiconductors** 

## **TYPICAL CHARACTERISTICS** ( $T_{amb} = 25$ °C, unless otherwise specified)

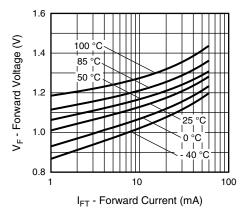


Fig. 2 - Forward Current vs. Forward Voltage

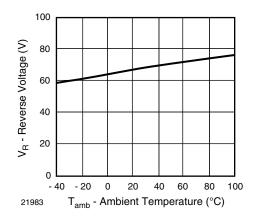


Fig. 3 - Reverse Voltage vs. Ambient Temperature

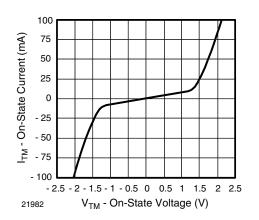


Fig. 4 - On-State Current vs. On-State Voltage

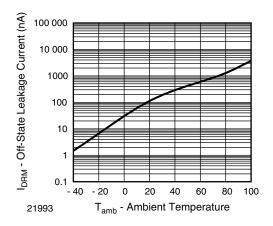


Fig. 5 - Off-State Leakage Current vs. Ambient Temperature

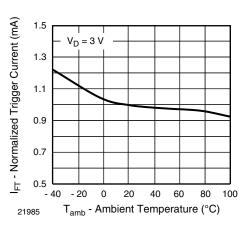


Fig. 6 - Normalized Trigger Current vs. Ambient Temperature

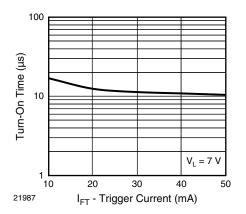


Fig. 7 - Trigger Current vs. Turn-On Time



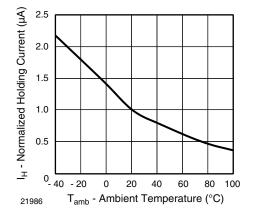


Fig. 8 - Normalized Holding Current vs. Ambient Temperature

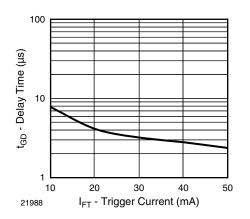


Fig. 9 - Trigger Current vs. Delay Time

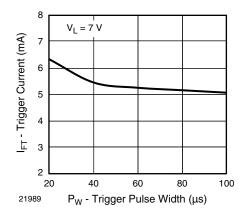


Fig. 10 - Trigger Current vs. Trigger Pulse Width

8 IFT - Trigger Current (mA) 7 6 5 4 3 0 100 200 300 400 500 600 21990 V<sub>Load</sub> - Load Voltage (V)

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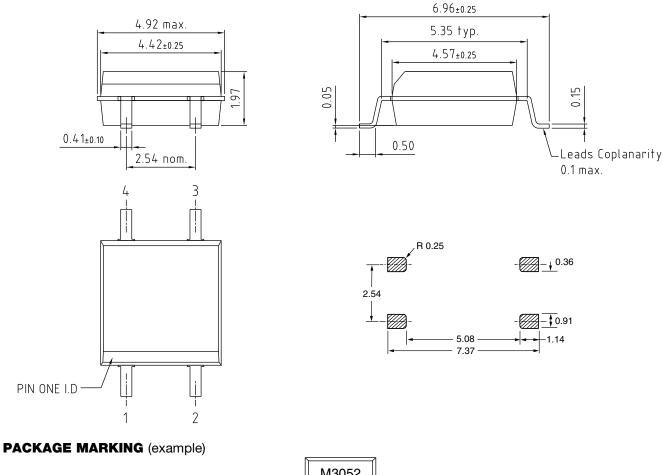
Fig. 11 - Trigger Current vs. Load Voltage

## **Vishay Semiconductors**

**PACKAGE DIMENSIONS** in millimeters

www.vishay.com

SHAY





### **TAPE AND REEL PACKAGING**

**Dimensions in millimeters** 

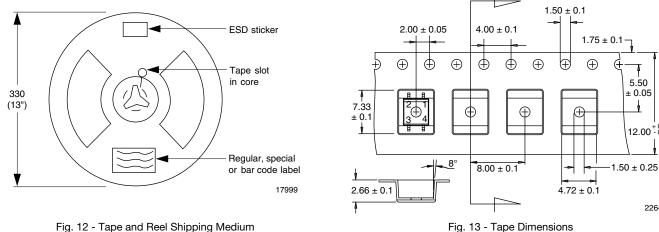


Fig. 12 - Tape and Reel Shipping Medium (EIA-481, revision A, and IEC 60286), 2000 units per reel

Rev. 1.3, 22-May-12

6

5.50 ± 0.05

ł

12.00<sup>+0.3</sup>-0.1

22646

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